
TCN75A Rev. A Silicon/Data Sheet Errata

The TCN75A parts you have received conform functionally to the TCN75A Data Sheet (DS21935A), except for the anomalies described below.

All of the issues listed here will be addressed in future revisions of the TCN75A silicon.

Contact Microchip for the latest Silicon fix.

1. **Module: Device Internal latch up at higher temperature**

The TCN75A devices may latchup during I²C communication at high temperature. This is caused by the internal I²C interface slew control circuit.

At temperatures > 80°C (typically), High-to-Low transition < 500 ns on the SDA or SCL lines causes the slew control circuit to latchup.

Work around

Connect a capacitor across the SDA and SCL pull-up resistors so that the SDA and SCL fall time is > 500 ns. For example, use 5 kΩ/0.01 μf.

Date Codes that pertain to this issue:

Not all production devices have these issues. Contact the Microchip Regional Sales Office for the fixed version.

2. **Module: POR active during oneshot mode**

At high temperature the TCN75A may reset while waking up from the oneshot Mode. All registers may be set the power up default states.

Work around

Shutdown and wake the device using the Shutdown bit in the configuration register.

Date Codes that pertain to this issue:

Not all production devices have these issues. Contact the Microchip Regional Sales Office for the fixed version.

Clarification/Corrections to the Data Sheet:

In the TCN75A Data Sheet (DS21935A), the following clarifications and corrections should be noted:

Work around

- none

APPENDIX A: REVISION HISTORY

Revision A (July 2007)

- Device Internal latch up at higher temperature
- POR active during oneshot mode

Note the following details of the code protection feature on Microchip devices:

- Microchip products meet the specification contained in their particular Microchip Data Sheet.
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