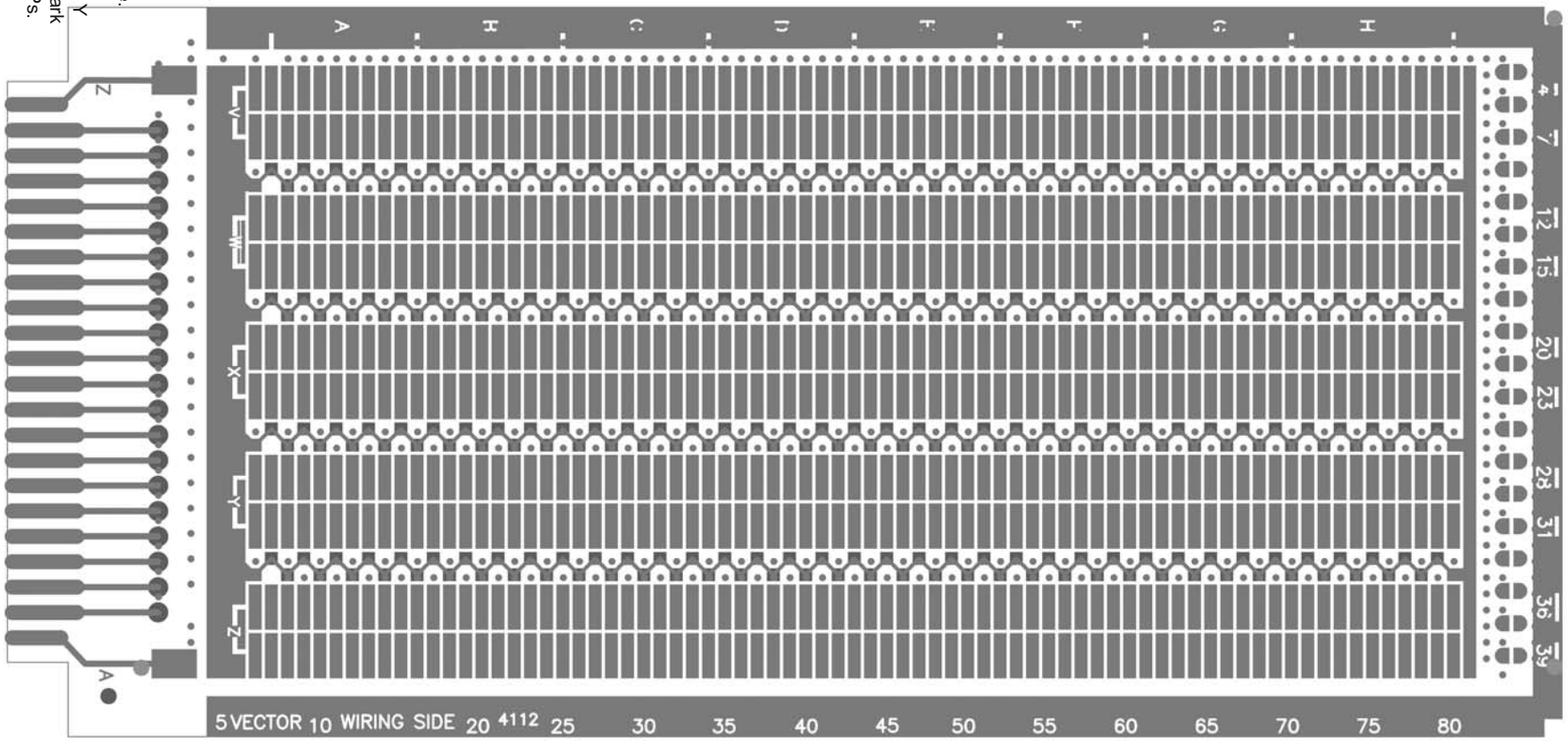
4112 9.6” LONG CARD
WIRING SIDE

1 DIP Position = center hole.

Zone letters A, B, C, etc., on Y axis and X, Y, Z on X axis mark position for 14- or 16-pin DIPs.



5. In accordance with consumer protection policies, we suggest you inspect the board before assembly to verify adequate clearance will exist between the ground plane surrounding the holes and any leads or terminals installed in holes so that shorting will not occur. Terminals inserted into the holes in the ground plane areas, and closely spaced contact finger areas may cause short circuits unless:

(1) The terminal’s shape is such that the maximum diagonal protrusion of the terminal cannot reach any **adjacent conductor.**

OR

(2) Special terminal installation tools are used which limit the installation of the terminal head such that a minimum gap of 0.012” remains between the surface of any **adjacent conductor** and the head of the terminal.

4. Before pressing terminals into board, position (rotate) terminals to maximize the clearance between the widest part of the terminal and the nearest adjacent conductor.

3. In any plug contact area on either side of Plugbord™, use only those holes having pads. Holes without pads may have insufficient clearance to adjacent circuitry, and using them could cause shorting.

2. Where tin coated circuitry exists, a small percentage of the holes may have solder blockage. This is usually a light “skin” easily penetrated by component leads. In some cases, a soldering iron may be required.

1. Intended for use in nonhostile environment up to 200 volts RMS or 300 volts DC.

NOTES:

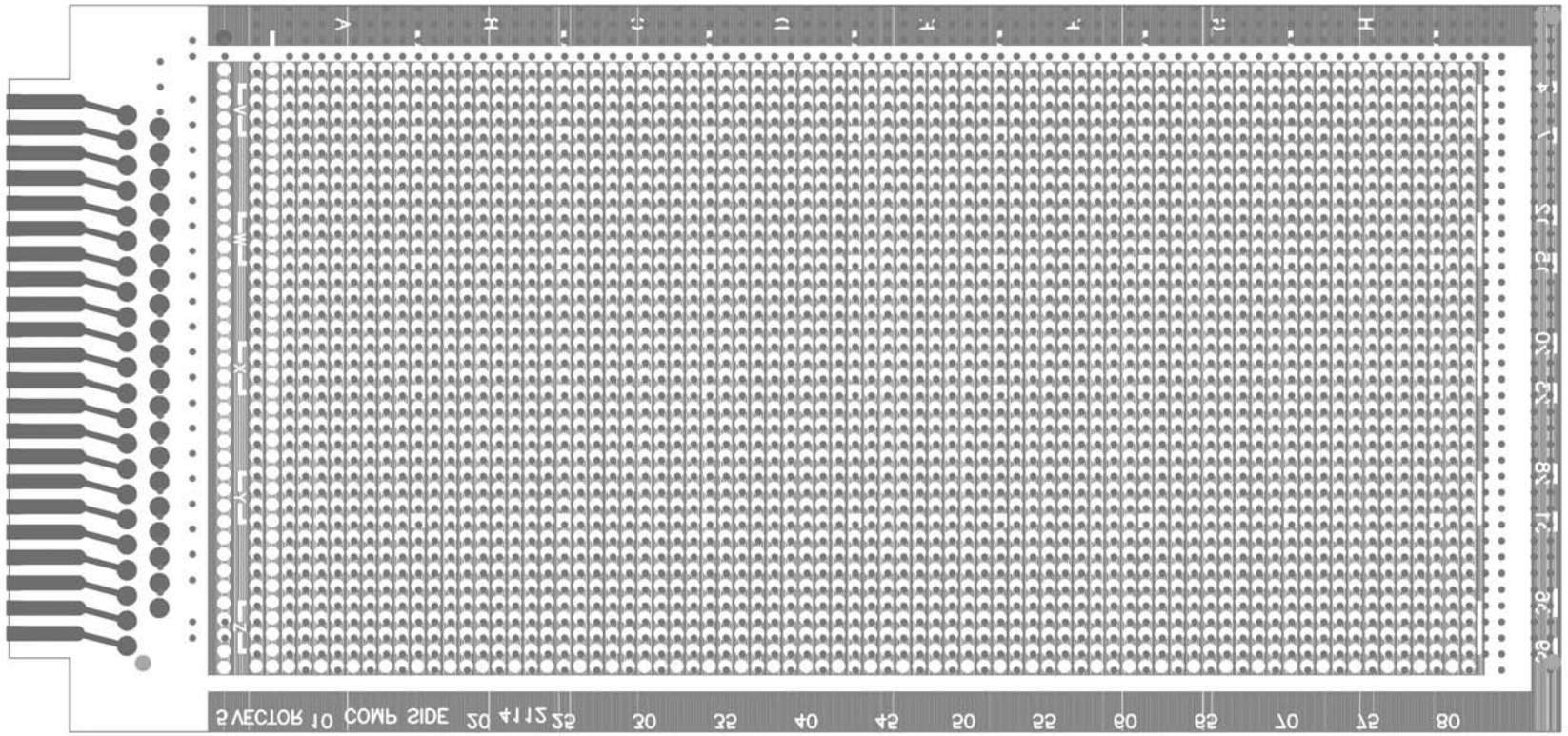
VECTOR
ELECTRONICS & TECHNOLOGY, INC.

VECTOR DIP LOGIC PLUGBORD™
PATTERN 0.042” X 0.1” SPACED HOLES
LA7-P8 LAYOUT PAPER

4112 9.6” LONG CARD
COMPONENT SIDE

1 DIP Position = Blocked Out Pad

Zone letters A, B, C, etc., on Y axis and X, Y, Z on X axis mark position for 14- or 16-pin DIPs.



5. In accordance with consumer protection policies, we suggest you inspect the board before assembly to verify adequate clearance will exist between the ground plane surrounding the holes and any leads or terminals installed in holes so that shorting will not occur. Terminals inserted into the holes in the ground plane areas, and closely spaced contact finger areas may cause short circuits unless:

(1) The terminal’s shape is such that the maximum diagonal protrusion of the terminal cannot reach any **adjacent conductor.**

OR

(2) Special terminal installation tools are used which limit the installation of the terminal head such that a minimum gap of 0.012” remains between the surface of any **adjacent conductor** and the head of the terminal.

OR

(3) The terminals are installed thru Vector’s T114 insulator strip preventing the terminal head from contacting any **adjacent conductor**.

4. Before pressing terminals into board, position (rotate) terminals to maximize the clearance between the widest part of the terminal and the nearest adjacent conductor.

3. In any plug contact area on either side of Plugbord[tm], use only those holes having pads. Holes without pads may have insufficient clearance to adjacent circuitry, and using them could cause shorting.

2. Where tin coated circuitry exists, a small percentage of the holes may have solder blockage. This is usually a light “skin” easily penetrated by component leads. In some cases, a soldering iron may be required.

1. Intended for use in nonhostile environment up to 200 volts RMS or 300 volts DC.

NOTES:

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VECTOR DIP LOGIC PLUGBORD™
PATTERN 0.042” X 0.1” SPACED HOLES
LA7-P9 LAYOUT PAPER Issue 81-08