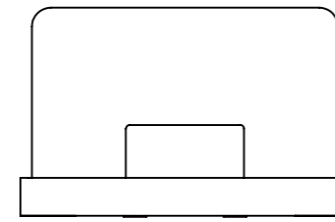


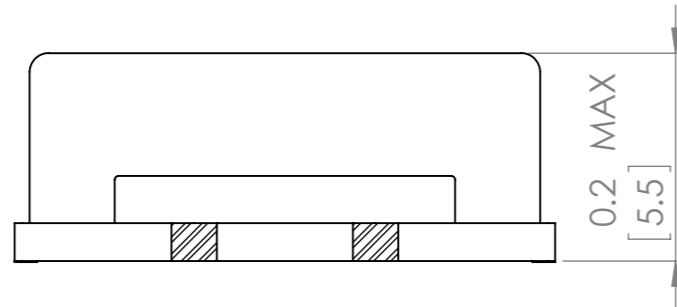


Top View

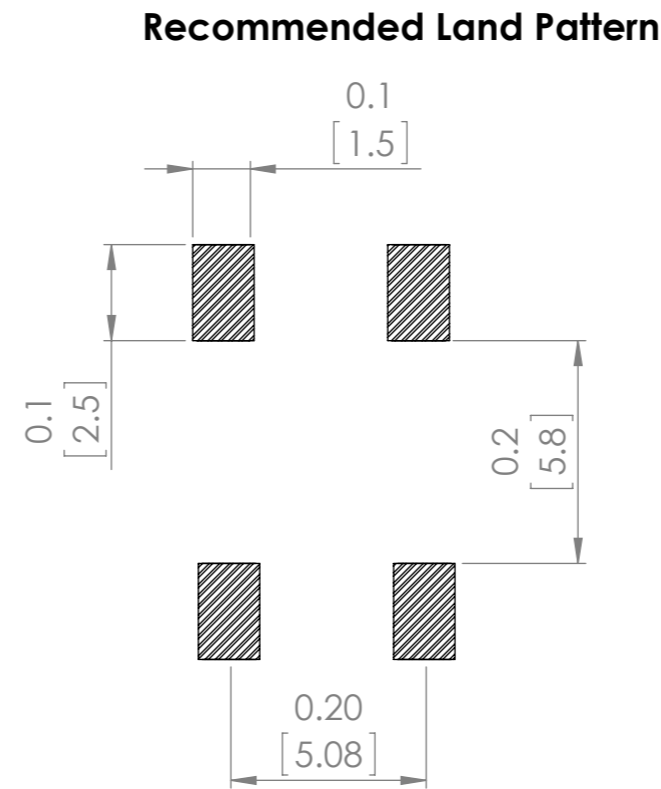


Side View

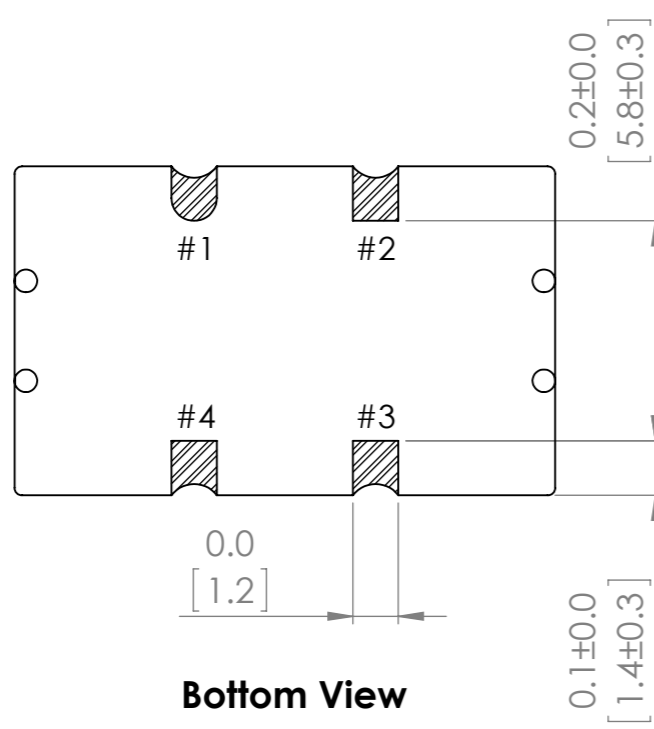
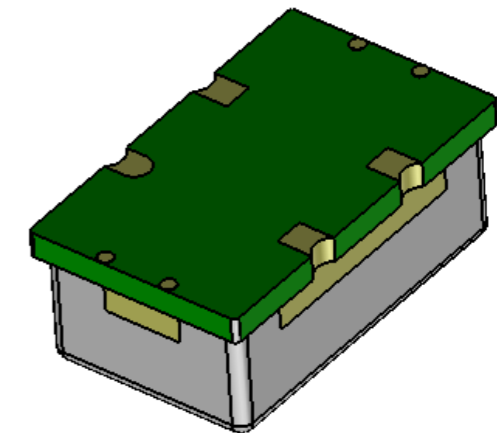
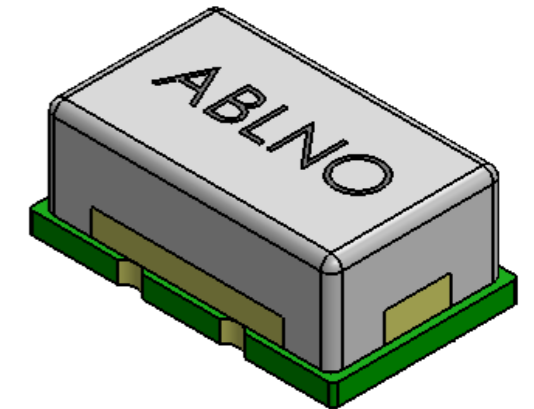
Pin #	Functionality
1	Voltage Control (vc) for VCXO NC for XO
2	Ground
3	RF Output
4	Vdd



Front View




Recommended Land Pattern



Bottom View

Note:
Packing overall Dimension 9.2x14.8x5.5mm SMT

UNLESS OTHERWISE SPECIFIED: DIMENSIONS ARE IN MM SURFACE FINISH: TOLERANCES: LINEAR: ANGULAR:				FINISH:		DEBUR AND BREAK SHARP EDGES		DO NOT SCALE DRAWING		REVISION -	
DRAWN SAAZVAT		SIGNATURE		DATE				 30332 Esperanza, Rancho Santa margarita, California 92688		TITLE: Ultra Low Phase Noise VCXO	
CHK'D XXXXXX											
APPV'D								DWG NO.		A3	
MFG								SCALE:4:1		SHEET 1 OF 1	
Q.A											
						MATERIAL:					
						WEIGHT:					